

Amendments of the Drawings

The attached Replacement Sheet 2 of the drawings includes changes to FIG. 2. This sheet, which includes only FIG. 2, replaces the original Sheet 2 including only FIG. 2. In replacement FIG. 2, the drawing is identified as -- PRIOR ART --.

Attachment: Replacement Sheet 2 of 7

Annotated Sheet 2 of 7 Showing Changes

## REMARKS

### Summary of Office Action

Claims 1-49 are pending in the above-identified patent application.

The Examiner has rejected claims 34 and 37-46 as being anticipated by Agrawal et al. U.S. Patent 6,097,212. Claims 35 and 36 have been objected to as being dependent from a rejected base claim, but allowable subject matter has been indicated. Claims 1-33, 47 and 48 have been allowed.

The Examiner has also objected to FIG. 2 of the drawings.

### Summary of Applicants' Reply

Applicants note with appreciation the allowance of claims 1-33, 47 and 48, and the indication of allowable subject matter in claims 35 and 36. Applicants hereby expressly reserve the right to rewrite either or both of claims 35 and 36 in independent form should the base claim ultimately not be allowed.

Applicants have re-presented Paragraph [0037] of the specification to clarify a table embedded therein.

Applicants have proposed an amendment of FIG. 2 of the drawings and respectfully request that that amendment be entered.

### Amendment of the Specification

Applicants have re-presented Paragraph [0037] of the specification to clarify a table embedded therein.

Paragraph [0037] includes a table embedded within the text of the paragraph. In addition to its header row, the table includes four rows and six columns. In the fifth and sixth columns only, each row is subdivided into two sub-rows. In the application as filed, Paragraph [0037] broke across the boundary between two pages of the specification (pages 12 and 13), and specifically broke between the sub-rows of the last row of the table embedded in the paragraph. Applicants have re-presented Paragraph [0037] as an "amended" paragraph, although no actual amendment was made.

The re-presentation of Paragraph [0037] is intended only to clarify the appearance of the table, which, in the re-presented form, appears all on one page (as does the entire paragraph).

Applicants' Reply to the  
Objection to the Drawings

The Examiner has objected to FIG. 2 of the drawings, requiring that it be labelled "PRIOR ART." Applicants have proposed an amendment of FIG. 2 of the drawings to label it as required by the Examiner, and respectfully request that that amendment be entered and that the objection be withdrawn.

Applicants' Reply to  
The Prior Art Rejection

Claims 34 and 37-46 have been rejected under 35 U.S.C. § 102(b) as being anticipated by Agrawal. Claims 35 and 36 have been objected to as being dependent from a rejected base claim, but allowable subject matter has been indicated. The rejection and objection are respectfully traversed.

Claim 34 defines a multiplexer circuit that includes, in pertinent part, a number,  $L$ , of look-up tables, and a number,  $C$ , of control inputs, where  $C = L+1$ . The Examiner cites FIG. 11B of Agrawal as showing such a circuit.

Applicants respectfully submit that the structure shown in FIG. 11B of Agrawal does not meet the limitations of claim 34. Claim 34 defines "[a] multiplexer circuit comprising:  $L$  look-up tables ... and  $C$  control inputs; wherein:  $C = L+1$ ." The control inputs of claim 34 are control inputs of the multiplexer circuit, not of components internal to the multiplexer circuit. As defined by claim 34, the number of control inputs of the claimed multiplexer circuit exceeds by one the number of look-up tables in the claimed multiplexer circuit.

In Agrawal FIG. 11B, multiplexer circuit 1100B has two look-up tables -- LUT 'a' and LUT 'b' ( $L = 2$ ) and two control inputs -- Select0 and Select1. A multiplexer

circuit according to applicants' claim 34 having two look-up tables would have three control inputs ( $L+1 = 3$ ).

The Examiner finds three control inputs in multiplexer circuit 1100B of FIG. 11B of Agrawal because one control input (Select0) may be connected to both of the look-up tables (depending on the state of PIP<sub>0</sub>) of multiplexer circuit 1100B. The Examiner thus characterizes Select0 and Select1 as three inputs -- one occurrence of Select0 directed to LUT 'a,' one occurrence of Select0 directed to LUT 'b,' and Select1 directed to the two-to-one multiplexer on the right side in FIG. 11B. However, applicants' claim 34 defines the number of control inputs of the multiplexer circuit, not the number of control inputs to internal components of the multiplexer circuit. In FIG. 11B of Agrawal, the number of control inputs to multiplexer circuit 1100B equals the number of look-up tables in multiplexer circuit 1100B, rather exceeding the number of look-up tables by one. Specifically, in Agrawal  $C = 2 = L \neq L+1$ . Thus, Agrawal FIG. 11B does not show or suggest the claimed invention. Moreover, applicants' claimed circuit is not shown anywhere else in Agrawal.

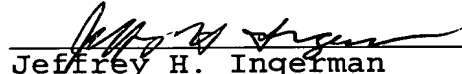
Accordingly, applicants respectfully submit that claim 34 is not anticipated by Agrawal and is patentable. Each of claims 35-46 depends, directly or indirectly, from claim 34, and accordingly each of claims 35-46 is patentable as well.

### Conclusion

For the reasons set forth above, applicants respectfully submit that this application, as amended, is in

condition for allowance. Reconsideration and prompt allowance of this application are respectfully requested.

Respectfully submitted,

  
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Jeffrey H. Ingerman  
Reg. No. 31,069  
Attorney for Applicants  
FISH & NEAVE IP GROUP  
ROPES & GRAY LLP  
Customer No. 36981  
1251 Avenue of the Americas  
New York, New York 10020-1105  
Tel.: (212) 596-9000

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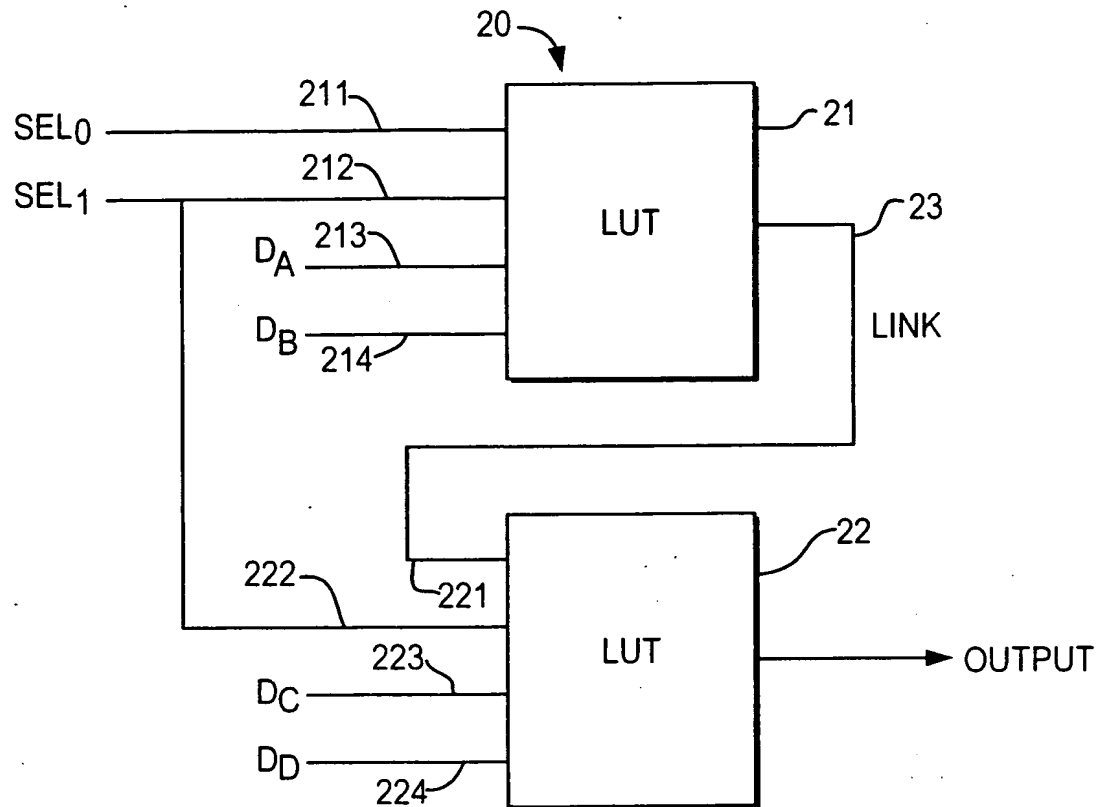


FIG. 2

PRIOR ART